

## **FEATURES**

- -33 to +5 dB Gain (user adjustable)
- · Accepts single-ended or differential inputs
- · Differential output
- High Linearity, Low Distortion
- 37 dB of Gain Control Range
- · Wideband Operation: to Above 1.1 GHz
- 4.5 dB Typical Noise Figure at 5 dB Gain
- Single +5 V Supply
- RoHS Compliant/Lead-Free Package
- 3 mm x 3 mm x 1 mm QFN Package

## APPLICATIONS

- CATV Digital Set Top Boxes
- Television Receiver Front-Ends
- Replacement for Passive Balun with Separate Gain Stage

## **PRODUCT DESCRIPTION**

The ABA3130 is a low noise amplifier with gain control that accepts a single-ended or differential RF input in the 50 MHz to 1.1 GHz frequency range and provides a balanced RF output with minimal degradation in signal quality. This highly integrated device amplifies the input signal using a highly linear, low noise amplification stage. Alternately, it can be used as a signal attentuator using the appropriate gain/attenuation control voltage. The overall linearity is maintained across a wide gain

control range, ensuring low distortion effects on the output signal. Requiring a single +5 V supply, the ABA3130 design is implemented using high-reliability GaAs MESFET process. The small, surface mount QFN packaging makes this device ideal for use in Cable TV set-top boxes, television receiver frontends, and other low noise applications. The device is characterized for both 75  $\Omega$  and 50  $\Omega$  systems.



## Figure 1: Functional Block Diagrams

# 50-1100 MHz Low Noise Amplifier/ Attenuator with Gain Control PRELIMINARY DATA SHEET



# ABA3130



Figure 2: Pinout (X-ray Top View)

| PIN | NAME   | DESCRIPTION              |
|-----|--------|--------------------------|
| 1   | RF⊪+   | RF Input (+)             |
| 2   | RF⊪    | RF Input (-)             |
| 3   | GND    | Ground                   |
| 4   | GND    | Ground                   |
| 5   | RFout+ | RF Output (+)            |
| 6   | RFout- | RF Output (-)            |
| 7   | VAGC   | Gain/Attenuation Control |
| 8   | GND    | Ground                   |
| 9   | Vdd    | Supply Voltage           |
| 10  | GND    | Ground                   |
| 11  | VBIAS1 | Bias1 Voltage            |
| 12  | VBIAS2 | Bias2 Voltage            |

| Table | 1: | Pin | Descri | ption |
|-------|----|-----|--------|-------|
|-------|----|-----|--------|-------|

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| PARAMETER                | MIN | MAX | UNIT | COMMENTS |
|--------------------------|-----|-----|------|----------|
| Supply Voltage (Vcc)     | 0   | +8  | V    |          |
| AGC Input Voltage (VAGC) | 0   | +5  | V    |          |
| RF Input Power (Pℕ)      | -   | +25 | dBmV |          |

Table 2: Absolute Minimum and Maximum Ratings

Stresses in excess of the absolute ratings may cause permanent damage. Functional operation is not implied under these conditions. Exposure to absolute ratings for extended periods of time may adversely affect reliability.

| PARAMETER                        | MIN  | ТҮР  | MAX  | UNIT | COMMENTS |
|----------------------------------|------|------|------|------|----------|
| Operating Frequency (f)          | 50   | -    | 1100 | MHz  |          |
| Supply Voltage (VDD)             | +3.3 | +5.0 | +5.5 | V    |          |
| AGC Input Voltage (VAGC)         | 0    | -    | +5   | V    |          |
| RF Input Power (P <sub>ℕ</sub> ) | -15  | -    | +15  | dBmV |          |
| Case Temperature (Tc)            | -40  | -    | +85  | °C   |          |

Table 3: Operating Ranges

The device may be operated safely over these conditions; however, parametric performance is guaranteed only over the conditions defined in the electrical specifications.

## ABA3130

| PARAMETER                    | MIN | ТҮР | MAX | UNIT | COMMENTS          |
|------------------------------|-----|-----|-----|------|-------------------|
| Gain at Maximum VAGC         | 4   | 4.3 | 6   | dB   | at 55 MHz         |
| Gain at Maximum VAGC         | 2.8 | -   | I   | dB   | at 853 MHz        |
| Gain at Minimum VAGC         | -41 | -33 | -30 | dB   | at 55 MHz         |
| Gain Flatness                | I   | □1  | I   | dB   |                   |
| AGC Input Voltage (VAGC)     | 0.3 | -   | 3.0 | V    | Max. Gain at +3 V |
| Noise Figure at Maximum Gain | -   | 4.5 | 5   | dB   | VAGC = +3 V       |
| CTB <sup>(1)</sup>           | -   | -63 | -50 | dBc  |                   |
| CSO <sup>(1)</sup>           | -   | -62 | -48 | dBc  |                   |
| XMOD <sup>(1)</sup>          | -   | -63 | I   | dBc  |                   |
| Reverse Isolation            | -   | -30 | -   | dB   |                   |
| Input Return Loss            | -13 | -   | -   | dB   | 75 🗌 single-ended |
| Output Return Loss           | -13 | -   | -   | dB   | 75 🗌 differential |
| Supply Current (IDD)         | 50  | 80  | 100 | mA   |                   |

Table 4: Electrical Specifications - 75  $\Omega$  system (Tc = +25 °C, V<sub>DD</sub> = +5 V, I<sub>DD</sub> = 80 mA)

Notes:

(1) 79 flat analog channels at +15 dBmV input power, plus 53 digital channels at +9 dBmV input power, and an output power of -7 dBmV.

|  |     | •,•••        | •••• | 101117     |  |
|--|-----|--------------|------|------------|--|
| PARAMETER                              | MIN | TYP          | MAX  | UNIT       | COMMENTS   |
| Gain at Maximum VAGC                   | -   | 4.2          | -    | dB         | at 450 MHz   |
| Gain at Minimum VAGC                   | -   | -34          | -    | dB         | at 450 MHz   |
| Gain Flatness                          | -   | □1           | -    | dB         |  |
| AGC Input Voltage (VAGC)               | 0.3 | I            | 3.0  | V          | Max. Gain at +3 V  |
| Noise Figure at Maximum Gain           | -   | 4.2          | 5.5  | dB         | V <sub>AGC</sub> = +3.0 V  |
| OIP3                                   | -   | 15.5<br>15.0 | -    | dBm<br>dBm | two tones: 510 and 511 MHz<br>two tones: 860 and 861 MHz<br>Pout per tone = -5 dBm |
| OIP2                                   | -   | 51.0<br>48.6 | -    | dBm<br>dBm | two tones: 222 and 228 MHz<br>two tones: 427 and 433 MHz<br>Pour per tone = -5 dBm |
| P1dB (Output power at 1dB compression) | -   | 8.3          | -    | dBm        | at 450 MHz, V <sub>AGC</sub> = 3.0 V   |

Table 5: Electrical Specifications - 50  $\Omega$  system (T<sub>c</sub> = +25 °C, V<sub>DD</sub> = +5 V, I<sub>DD</sub> = 78 mA)

Notes:

(1) 50  $\Omega$  tests measured using Figure 18, but with C1= 0.5 pF and L3 = 5.6 nH.

(2) Tested with balun on output (see Figure 18).

## **PERFORMANCE DATA - 75** $\Omega$ System







Figure 7: Noise Figure vs. Frequency  $(T_c = +25 \ ^\circ C, V_{DD} = +5 \ V, V_{AGC} = +3 \ V)$ 



Figure 4: Input Return Loss (S11) vs. Frequency (Tc = +25 °C, VDD = +5 V)



Figure 6: Output Return Loss (S22) vs. Frequency ( $T_c = +25 \ ^\circ C$ ,  $V_{DD} = +5 \ V$ )









## PERFORMANCE DATA - 50 $\Omega$ System

Figure 13: Gain (S21) vs. Frequency (Tc = +25 °C, V<sub>DD</sub> = +5 V)



Figure 15: Reverse Isolation (S12) vs. Frequency ( $T_c = +25 \ ^\circ C$ ,  $V_{DD} = +5 \ V$ ,  $V_{AGC} = +3 \ V$ )



Figure 17: Gain and Noise Figure vs. Frequency (V<sub>DD</sub> = +5 V, V<sub>AGC</sub> = +3 V, I<sub>DD</sub> = 75mA)



#### Note:

50  $\Omega$  characterization is for reference only. Part is not tested at 50  $\Omega$  in production.

## Figure 14: Input Return Loss (S11) vs. Frequency (Tc = +25 °C, $V_{DD}$ = +5 V)



Figure 16: Output Return Loss (S22) vs. Frequency ( $T_c = +25 \ ^\circ C$ ,  $V_{DD} = +5 \ V$ )



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## **APPLICATION INFORMATION**



Figure 18: Test Circuit

\*Note: C1 & L3 are required for matching to a 75  $\Omega$  low cost F connector at the RF Input. Higher quality connectors may not require such matching.

#### Preliminary Data Sheet





Figure 19: Balun Detail - 3.5 turn (P8002458)

Notes: Ferrite core: FAIR-RITE #2843002702 Wire: MWS WIRE IND. #T-2361429-20 Balun Winding - 3 1/2 turns through core

## PACKAGE OUTLINE

¥∎°∟

A A1

b

D D1

E E1

е

K

L

11

DIMENSIONS-MM

3.00 BSC

0.50 BSC

MAX 1.00 0.05

0.30

1.70

0.55

0.15 MAX

MIN. 0.80 0.00

0.18

1.30

1.30

0.20 MIN

0.35



NOTES :

- 1. ALL DIMENSIONS ARE IN MILLIMETERS.
- 2. MAX. PACKAGE WARPAGE IS 0.05 mm.
- 3. MAXIMUM ALLOWABLE BURRS IS 0.076 mm IN ALL DIRECTIONS.
- A. PIN #1 ID ON TOP WILL BE LASER MARKED.

A MAXIMUM 0.15mm PULL BACK (L1) MAYBE PRESENT.

- L MINUS L1 TO BE EQUAL TO OR GREATER THAN 0.30mm.
- 6. DIMENSION & APPLIES TO METALLIZED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30mm FROM TERMINAL TIP. IF THE TERMINAL HAS THE OPTIONAL RADIUS ON THE OTHER END OF THE TERMINAL, THE DIMENSION & SHOULD NOT BE MEASURED IN THAT RADIUS AREA.
- $\bigtriangleup$  bilateral coplanarity zone applies to the exposed heat sink slug as well as the terminals.
- 8. REFERENCE JEDEC OUTLINE MO-220.

### Figure 15: S26 Package Outline - 12 Pin 3 mm x 3 mm x 1 mm QFN

DIMENSIONS-INCHES

0.118 BSC

MAX 0.039 0.001

0.011

0.067

0.067

0.022 0.006 MAX

MIN. 0.031 0.000

0.051

0.007 MIN.

0.014

01

0.019 BSC

Ň

A1

b

D D1

E E1

е

Κ

L

## ORDERING INFORMATION

| ORDER NUMBER  | TEMPERATURE<br>RANGE | PACKAGE<br>DESCRIPTION                                     | COMPONENT PACKAGING                 |
|---------------|----------------------|--|-------------------------------------|
| ABA3130RS26Q1 | -40 °C to +85 °C     | RoHS Compliant 12 Pin<br>3 mm x 3 mm x 1 mm<br>QFN Package | Tape and Reel, 1000 pieces per Reel |

NOTES

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